

PATENT NUMBER

U.S. UTILITY Patent Application

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APPLICATION NO. 09/961248	CONT/PRIOR F	CLASS 438	SUBCLASS 40	ART UNIT 2815	EXAMINER [Signature]
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APPLICANTS

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Power semiconductor device and manufacturing method thereof

PTO-2040
12/99[illegible]

TERMINAL DISCLAIMER	DRAWINGS		CLAIMS ALLOWED	
	Sheets Drwg.	Figs. Drwg.	Print Fig.	Total Claims
<input type="checkbox"/> The term of this patent subsequent to _____ (date) has been disclaimed.	_____ (Assistant Examiner)		NOTICE OF ALLOWANCE MAILED	
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